

UNISONIC TECHNOLOGIES CO., LTD

2SD880

NPN SILICON TRANSISTOR

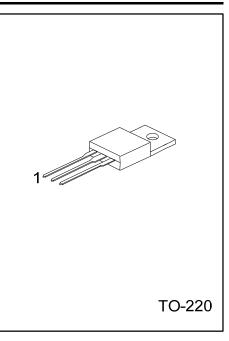
NPN EPITAXIAL TRANSISTOR

DESCRIPTION

The UTC 2SD880 is designed for audio frequency power amplifier applications.

FEATURES

- * High DC Current Gain: h_{FE} =200(Max.)(V_{CE} =5V, I_{C} =0.5A)
- * Low Saturation Voltage: $V_{CE(SAT)}$ =1.0V(Max.)(I_C=3A, I_B=0.3A) * High Power Dissipation: P_C=30W (T_A=25°C)
- * Complementary to 2SB834



ORDERING INFORMATION

Ordering Number		Dealise	Pin Assignment			Decking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
2SD880L-TA3-T	2SD880G-TA3-T	TO-220	В	С	E	Tube	

2SD880L-TA3-T	(1) T: Tube
(2)Package Type	(2) TA3: TO-220
(3)Lead Free	(3) G: Halogen Free, L: Lead Free

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	V _{CBO}	60	V
Collector to Emitter Voltage	V _{CEO}	60	V
Emitter to Base Voltage	V _{EBO}	7	V
Collector Current	lc	3	А
Base Current	I _B	0.5	А
Power Dissipation	PD	30	W
Junction Temperature	TJ	+150	°C
Storage Temperature	T _{STG}	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

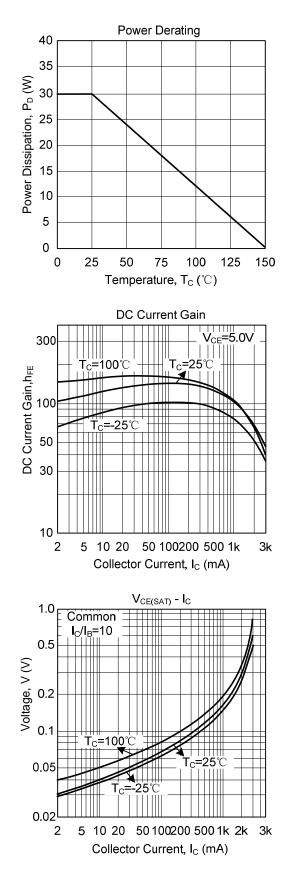
■ ELECTRICAL CHARACTERISTICS (T_A =25°C)

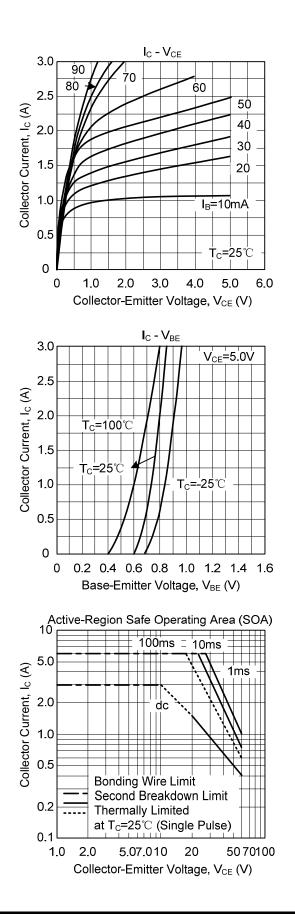
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Breakdown Voltage	BV_{CEO}	I _C =50mA, I _E =0	60			V
Collector Cut-Off Current	I _{CBO}	$V_{CB}=60V, I_{E}=0$			100	μA
Emitter Cut-Off Current	I _{EBO}	$V_{EB}=7V, I_{C}=0$			100	μA
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	I _C =3A, I _B =300mA			1	V
Base-Emitter Saturation Voltage	V _{BE(ON)}	V _{CE} =5V, I _C =500mA			1	V
DC Current Gain	h _{FE}	I _C =500mA, V _{CE} =5V	100		200	
Current gain bandwidth product	f _T	V _{CE} =5V, I _C =500mA		3		MHZ



2SD880

TYPICAL CHARACTERISTICS







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