



700mA HIGH VOLTAGE ADJUSTABLE CURRENT REGULATOR WITH ENABLE CONTROL

DESCRIPTION

The AMC7140 is a high voltage, low drop-out current regulator for maximum output current up to 700mA. The output current was decided by external resistor. The output sink current could be disabled via OE pin

Build-in thermal protection to prevent the chip over heat damage.

FEATURES

- 0.5V Output Drop-out Voltage at 700mA.
- 700mA Maximum Output Current.
- Output Current Controlled by External Resistor.
- 3us Fast Response Output Stage Enable Control.
- Output Driving Voltage Up To 75V.
- Supply Voltage Range 5V~50V
- TO-252-5L package

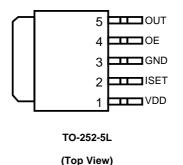
TYPICAL APPLICATION CIRCUIT

APPLICATIONS

- High Power LED Driver
- RGB Full Color Power LED driver
- LCD Monitor/TV LED backlight Driver
- LED Table Lamp

Enable OE AMC7140 ISET GND OUT

PACKAGE PIN OUT



ORDER INFORMATION				
DL	TO-252			
DL	5 pin			
	AMC7140DLFT (Lead Free)			

Note: All surface-mount packages are available in Tape & Reel. Append the letter "T" to part number (i.e. AMC7140DLFT). The letter "F" is marked for Lead Free process.



ABSOLUTE MAXIMUM RATINGS (Note)	
Input Voltage, V _{DD}	50V
Output Voltage, V _{OUT}	75V
Maximum Operating Junction Temperature, T _J	150°C
Storage Temperature Range	-65°C to 150°C
Lead Temperature (soldering, 10 seconds)	260°C
Note: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of the specified terminal.	

PIN DESCRIPTION				
Pin Name	Pin Function			
OUT	Output pin. Sink current is decided by the current on R_{SET} connected to I_{SET} . I_{OUT} = 500 \times I_{SET} .			
OE	Output stage enable control pin. High enable the OUT pin.			
I_{SET}	Output current set input. Connect a resistor from I_{SET} to GND to set the LED bias current. $I_{SET} = 1.2 V/R_{SET}$.			
VDD	Power supply.			
GND	Ground			

THERMAL DATA			
Thermal Resistance from Junction to Ambient, θ_{JA}	80 °C /W		
Junction Temperature Calculation: $T_J = T_A + (P_D \times \theta_{JA})$.			
The θ _{JA} numbers are guidelines for the thermal performance of the device/pc-board system. Connect the ground pin to ground using a large pad or ground plane for better heat dissipation.			
All of the above assume no ambient airflow.			

Maximum Power Calculation:

$$P_{D(MAX)} = \frac{T_{J(MAX)} - T_{A(MAX)}}{\theta_{IA}}$$

T_J(°C): Maximum recommended junction temperature

 $T_A(^{\circ}C)$: Ambient temperature of the application

 $\theta_{JA}(^{\circ}C/W)$: Junction-to-Ambient temperature thermal resistance of the package, and other heat dissipating materials.

The maximum power dissipation for a single-output regulator is:

 $P_{\text{D(MAX)}} \!=\! \left[\left(V_{\text{IN(MAX)}} \text{-} V_{\text{OUT(NOM)}} \right) \right] \times I_{\text{OUT(NOM)}} + V_{\text{IN(MAX)}} \! \times I_{\text{Q}}$

Where: $V_{OUT(NOM)}$ = the nominal output voltage

 $I_{OUT(NOM)}$ = the nominal output current, and

 I_Q = the quiescent current the regulator consumes at $I_{OUT(MAX)}$

 $V_{IN(MAX)}$ = the maximum input voltage

Then $\theta_{JA} = (+150 \,{}^{\circ}\text{C} - \text{T}_{A})/P_{D}$



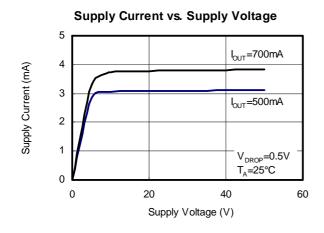
RECOMMENDED OPERATING CONDITIONS					
Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	$V_{ m DD}$	5		50	V
Output Sink Current	I_{OUT}			700	mA
Operating free-air temperature range	Та	-40		+85	$^{\circ}\!\mathbb{C}$

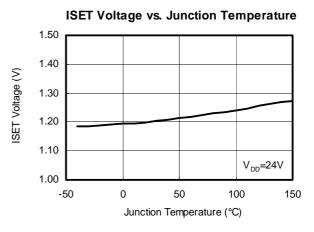
DC ELECTRICAL CHARACTERISTICS V _{DD} =24V, Ta=25°C, No Load, (Unless otherwise noted)						
						Parameter
	V_{OUT} =0.5V, R_{SET} =4K Ω		150			
Output Current	V_{OUT} =0.5V, R_{SET} =1.2K Ω		500		mA	
Outras Community Description	V_{OUT} =0.5V, R_{SET} =4K Ω			±5	07	
Output Current Deviation	V_{OUT} =0.5V, R_{SET} =1.2K Ω			±5	%	
SET Current Range		200		1400	μA	
Maximum Output Current	I _{SET} =1400uA		700		mA	
Output Drop-out Voltage*	I _{SET} =1000uA, Note 1		0.35		V	
Thermal Shut-down Junction Temperature	Hysteresis 20°C		160		$^{\circ}\!\mathbb{C}$	
"Low" Input Voltage		0		0.8	V	
"High" Input Voltage		2		12	V	
"Low" Input Current		-20		+20	μA	
"High" Input Current		-5.0		+5.0	μA	
Output Enable Delay Time	OE from Low to High, V_{OUT} =0.5V, I_{OUT} =350mA, 50%		3		μS	
Output Disable Delay Time	OE from High to Low, V_{OUT} =0.5V, I_{OUT} =350mA, 50%		3		μS	
Supply Current Consumption			-	5	mA	

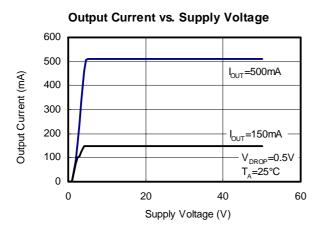
Note1: Output dropout voltage: 90% x I_{OUT} @ $V_{\text{OUT}}\!\!=\!\!500\text{mV}$

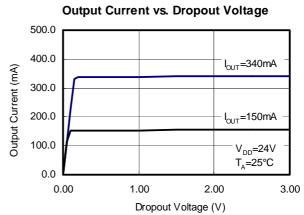


CHARACTERIZATION CURVES











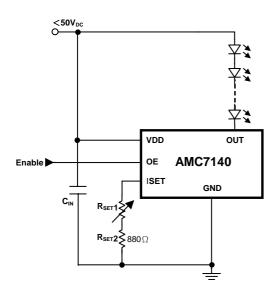
APPLICATION INFORMATION

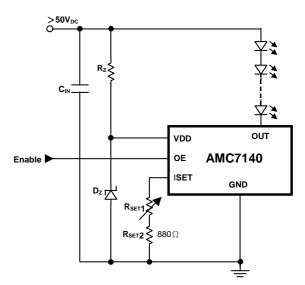
AMC7140 is a high voltage, low dropout current regulator for maximum output current up to 700mA with OE pin. The current could be linearly adjusted through variable resister connected to I_{SET} pin, or by PWM control via OE pin. Although the absolute maximum rating of OUT pin 60V, the dropout voltage between OUT pin and GND pin should not be too large when current is sinking because of the heat dissipation capability of the package.

Here are some of the typical application examples:

DC Voltage Input:

Any DC voltage level between 5V to 50V could be adopted as power source V_{DD} for typical application of AMC7140 as long as V_{DD} is larger than the total forward voltage drop of the LED string (at expecting current) by 0.35V. If $50V \sim 60V$ voltage level is adopted as power source to positive end of the LED string, one Zener shunt regulator could be used to provide appropriate voltage to VDD pin.

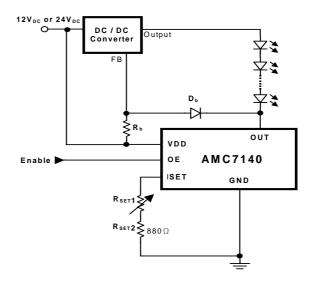






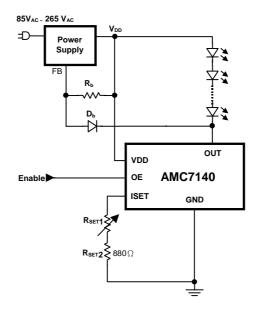
LED Backlight Solution:

AMC7140 could coordinate with any type of DC-to-DC converter through feedback path to realized LED backlight module. The number of LEDs in the string is variable even with certain fixed power source since the output voltage of the DC-to-DC converter could be modulated according to feedback signal.



AC Voltage Input:

AMC7140 could work with any kind of well-known or well-developed switch-mode power supply system. Simply cut off the internal feedback path of the power supply system and then feed the signal from AMC7140 back to the power supply system instead.





THERMAL CONSIDERATION

The Maximum Power Dissipation on Current Regulator:

 $P_{D(MAX)} = V_{OUT(MAX)} \times I_{OUT(NOM)} + V_{IN(MAX)} \times I_{DD}$

 $V_{OUT(MAX)}$ = the maximum voltage on output pin;

 $I_{OUT(NOM)}$ = the nominal output current;

 I_{DD} = the quiescent current the regulator consumes at $I_{OUT(NOM)}$;

 $V_{IN(MAX)}$ = the maximum input voltage.

Thermal Consideration:

The AMC7140 has internal power and thermal limiting circuitry designed to protect the device under overload conditions. However, maximum junction temperature ratings should not be exceeded under continuous normal load conditions. The thermal protection circuit of AMC7140 prevents the device from damage due to excessive power dissipation. When the device junction temperature rises to approximately 150°C, the regulator will be turned off. When power consumption is over about 1000mW (TO-252 package, at T_A =70°C), additional heat sink is required to control the junction temperature below 125°C.

The junction temperature is:

$$T_J = P_D (\theta_{JT} + \theta_{CS} + \theta_{SA}) + T_A$$

P_D: Dissipated power.

 θ _{JT}: Thermal resistance from the junction to the mounting tab of the package.

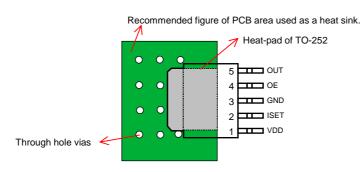
For TO-252 package, $\theta_{JT} = 7.0 \text{ oC/W}$.

 θ_{CS} : Thermal resistance through the interface between the IC and the surface on which it is mounted. (typically, $\theta_{CS} < 1.0^{\circ}\text{C/W}$)

 θ_{SA} . Thermal resistance from the mounting surface to ambient (thermal resistance of the heat sink).

If PC Board copper is going to be used as a heat sink, below table can be used to determine the appropriate size of copper foil required. For multi-layered PCB, these layers can also be used as a heat sink. They can be connected with several through-hole vias.

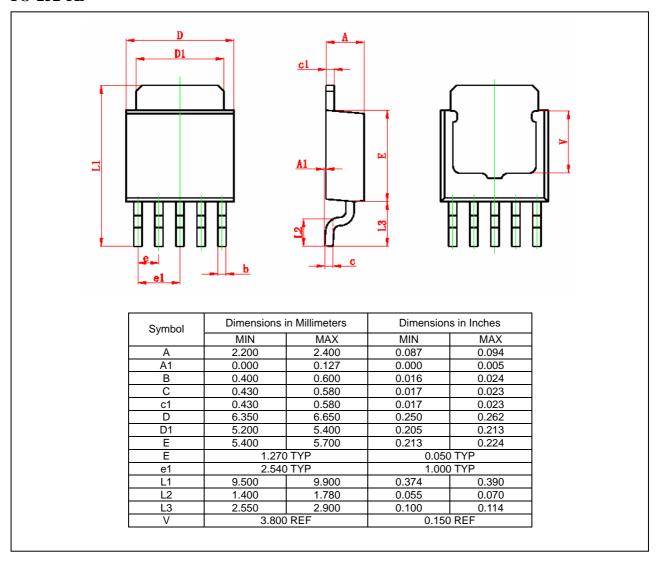
PCB θ sa (°C/W)	59	45	38	33	27	24	21
PCB heat sink size (mm ²)	500	1000	1500	2000	3000	4000	5000





PACKAGE

TO-252-5L





IMPORTANT NOTICE

ADDtek reserves the right to make changes to its products or to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

A few applications using integrated circuit products may involve potential risks of death, personal injury, or severe property or environmental damage. ADDtek integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life-support applications, devices or systems or other critical applications. Use of ADDtek products in such applications is understood to be fully at the risk of the customer. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.

ADDtek assumes to no liability to customer product design or application support. ADDtek warrants the performance of its products to the specifications applicable at the time of sale.

ADDtek Corp.

9F, No. 20, Sec. 3, Bade Rd., Taipei, Taiwan, 105

TEL: 2-25700299 FAX: 2-25700196